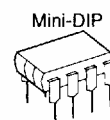
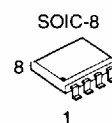
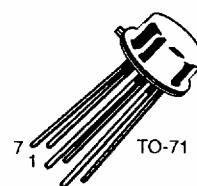
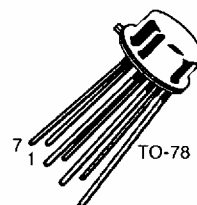


## N Channel

Device	$V_P$		$G_{fs}$ (mmho)		$V_{GS1-2}$ $V_{OS}$ (mV) Max	Drift ( $\mu V/C$ ) $\Delta V_{OS}$ Max	$I_{DSS}$ Match %	$G_{fs}$ Match %	Package
	(V) Min	Max	Min	Max					
2N3955	1	4.5	1	3	10	25	5	5	TO-71
2N3956	1	4.5	1	3	15	50	5	5	TO-71
2N3958	1	4.5	1	3	25	100	15	15	TO-71
2N5197	0.7	4.0	1	4	5	10	5	3	TO-71
2N5564	0.5	3	7.5	12.5	5	10	5	5	TO-71
2N5565	0.5	3	7.5	12.5	10	25	5	10	TO-71
2N5566	0.5	3	7.5	12.5	20	50	5	10	TO-71
2N5906	0.6	4.5	70 $\mu$	0.25	5	5	5	3	TO-78
2N5908	0.6	4.5	70 $\mu$	0.25	10	20	5	5	TO-78
2N5909	0.6	4.5	70 $\mu$	0.25	15	40	5	5	TO-78
2N5911	1	5	5	10	10	20	5	5	TO-78
2N5912	1	5	5	10	15	40	5	5	TO-78
2N6485	0.7	4	1	4	15	25	5	5	TO-71
NPDS5564	0.5	3	7.5	12.5	5	10	5	5	Mini-Dip
NPDS5565	0.5	3	7.5	12.5	10	25	5	10	Mini-Dip
NPDS5566	0.5	3	7.5	12.5	20	50	5	10	Mini-Dip
NPDS8303	0.5	3.5	1	4	15	115			Mini-Dip
NPDS404	0.5	2.5	2	7	15	25			SOIC-8
NPDS405	0.5	2.5	2	7	20	40			SOIC-8
NPDS406	0.5	2.5	2	7	40	80			SOIC-8
NPDS5564	0.5	3	7.5	12.5	5	10		5	SOIC-8
NPDS5565	0.5	3	7.5	12.5	10	25		10	SOIC-8
NPDS5566	0.5	3	7.5	12.5	20	50		10	SOIC-8
NPDS5911	1	5	5	10	10	20	5	5	SOIC-8
NPDS5912	1	5	5	10	15	40	5	5	SOIC-8
U257	1	5	5	10	100		15	15	TO-78
U401	0.5	2.5	2	7	5	10			TO-71
U403	0.5	2.5	2	7	10	25			TO-71
U406	0.5	2.5	2	7	40	80			TO-71

t=typical value



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