

LH0024 High Slew Rate Operational Amplifier

General Description

The LH0024/LH0024C is a very wide bandwidth, high slew rate operational amplifier intended to fulfill a wide variety of high speed applications such as buffers to A to D and D to A converters and high speed comparators. The device exhibits useful gain in excess of 50 MHz making it possible to use in video applications requiring higher gain accuracy than is usually associated with such amplifiers.

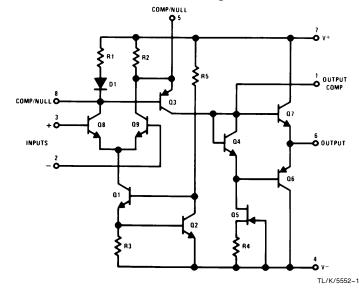
The LH0024/LH0024C's combination of wide bandwidth and high slew rate make it an ideal choice for a variety of high speed applications including active filters, oscillators, and comparators as well as many high speed general purpose applications.

The LH0024 is guaranteed over the temperature range -55°C to $+125^{\circ}\text{C}$, whereas the LH0024C is guaranteed -25°C to $+85^{\circ}\text{C}$.

Features

- Very high slew rate—500 V/ μ s at A_V = +1
- Wide small signal bandwidth—70 MHz
- Wide large signal bandwidth—15 MHz
- High output swing—±12V into 1k
- Low input offset—2 mV
- Pin compatible with standard IC op amps

Schematic and Connection Diagrams



Metal Can Package COMP/NULL V+ IN (-) 2 6 OUTPUT IN (+) 3 4 3 COMP/NULL

Top View

TL/K/5552-2

Note: For heat sink use Thermalloy 2230-5

Order Number LH0024H, LH0024H-MIL or LH0024CH See NS Package Number H08B

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. (Note 2)

Supply Voltage± 18VInput VoltageEqual to SupplyDifferential Input Voltage± 5VPower Dissipation600 mW

Storage Temperature Range $$-65^{\circ}\text{C}$\ to +150^{\circ}\text{C}$$ Lead Temperature (Soldering, 10 sec.) $$260^{\circ}\text{C}$$ ESD rating to be determined.

Operating Temperature Range

DC Electrical Characteristics (Note 1)

Parameter	Conditions	LH0024			LH0024C			Units
raiailletei		Min	Тур	Max	Min	Тур	Max	Ullita
Input Offset Voltage	$R_S = 50\Omega$, $T_A = 25^{\circ}C$ $R_S = 50\Omega$		2.0	4.0 6.0		5.0	8.0 10.0	mV mV
Average Temperature Coefficient of Input Offset Voltage	$V_{\rm S}=\pm 15 {\rm V}, {\rm R}_{\rm S}=50 \Omega$ $-55^{\circ}{\rm C}$ to 125°C		-20			-25		μV/°C
Input Offset Current	$T_A = 25^{\circ}C$		2.0	5.0 10.0		4.0	15.0 20.0	μA μA
Input Bias Current	$T_A = 25^{\circ}C$		15	30 40		18	40 50	μA μA
Supply Current			12.5	15		12.5	15	mA
Large Signal Voltage Gain	$V_S = \pm 15V, R_L = 1k, T_A = 25^{\circ}C$ $V_S = \pm 15V, R_L = 1k$	4 3	5		3 2.5	4		V/mV V/mV
Input Voltage Range	V _S = ±15V	±12	±13		±12	±13		٧
Output Voltage Swing	$V_S = \pm 15V, R_L = 1k, T_A = 25^{\circ}C$ $V_S = \pm 15V, R_L = 1k$	±12 ±10	±13		±10 ±10	±13		V V
Slew Rate	$V_S = \pm 15V, R_L = 1k,$ $C_1 = C_2 = 30 \text{ pF},$ $A_V = +1, T_A = 25^{\circ}\text{C}$	400	500		250	400		V/μs
Common-Mode Rejection Ratio	$V_S = \pm 15V$, $\Delta V_{IN} = \pm 10V$, $R_S = 50\Omega$		60			60		dB
Power Supply Rejection Ratio	$\pm 5V \le V_S \le \pm 18V,$ $R_S = 50\Omega$		60			60		dB

Note 1: These specifications apply for $V_S = \pm 15V$ and $-55^{\circ}C$ to $+125^{\circ}C$ for the LH0024 and $-25^{\circ}C$ to $+85^{\circ}C$ for the LH0024C.

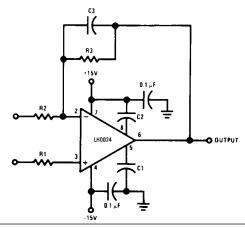
Note 2: Refer to RETS0024 for LH0024H military specifications.

Frequency Compensation

TABLE I

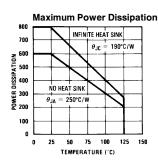
Closed Loop Gain	C ₁	C ₂	C ₃	
100	0	0	0	
20	0	0	0	
10	0	20 pF	1 pF	
1	30 pF	30 pF	3 pF	

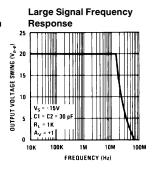
Frequency Compensation Circuit

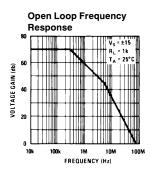


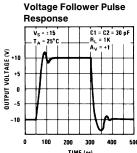
TL/K/5552-6

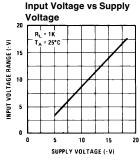
Typical Performance Characteristics

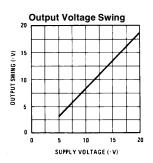


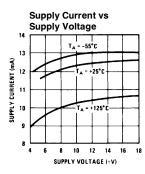


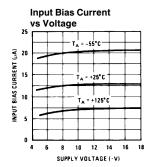












TL/K/5552-7

Applications Information

LAYOUT CONSIDERATIONS

The LH0024/LH0024C, like most high speed circuitry, is sensitive to layout and stray capacitance. Power supplies should be by-passed as near the device as is practicable with at least 0.01 $\mu\mathrm{F}$ disc type capacitors. Compensating capacitors should also be placed as close to device as possible.

COMPENSATION RECOMMENDATIONS

Compensation schemes recommended in Table 1 work well under typical conditions. However, poor layout and long lead lengths can degrade the performance of the LH0024 or cause the device to oscillate. Slight adjustments in the values for C1, C2, and C3 may be necessary for a given layout. In particular, when operating at a gain of -1, C3 may re-

quire adjustment in order to perfectly cancel the input capacitance of the device.

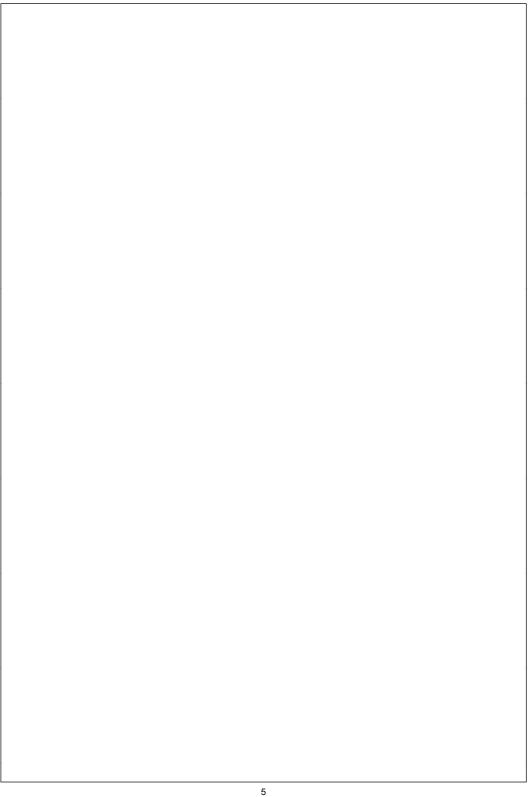
When operating the LH0024/LH0024C at a gain of $\pm\,1$, the value of R1 should be at least 1 $k\Omega.$

The case of the LH0024 is electrically isolated from the circuit; hence, it may be advantageous to drive the case in order to minimize stray capacitances.

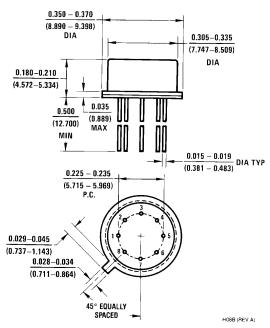
HEAT SINKING

The LH0024/LH0024C is specified for operation without the use of an explicit heat sink. However, internal power dissipation does cause a significant temperature rise. Improved offset voltage drift can be obtained by limiting the temperature rise with a clip-on heat sink such as the Thermalloy 2228B or equivalent.

Typical Applications TTL Compatible Comparator Offset Null 10K +15V +15V 10K LH0024 2N2369 -15V LH0024 O OUTPUT TL/K/5552-3 TL/K/5552-4 Video Amplifier R3 10K R1 = R2 = R3 = R4 $\frac{R5 + (R3 R4)}{(R3 R4)} = 5$ (R3) (R4) TL/K/5552-5



Physical Dimensions inches (millimeters)



Metal Can Package (H)
Order Number LH0024H, LH0024H-MIL or LH0024CH
NS Package Number H08B

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Datasheets for electronics components.

National Semiconductor was acquired by Texas Instruments.

http://www.ti.com/corp/docs/investor_relations/pr_09_23_2011_national_semiconductor.html

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LH0024 - http://www.ti.com/product/lh0024?HQS=TI-null-null-dscatalog-df-pf-null-wwe

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